



multi-core processor testing

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Multiprocessor Systems - group of 4 »

JD Davis, L Hammond, K Olukotun - In Workshop on Architecture Research Using FPGA Platforms, ..., 2005 - cag.csail.mit.edu

A Flexible Architecture for Simulation and Testing (FAST) ... Hub of the **multi-core** system ...

Processor Tile 1 Processor Tile 2 Processor Tile 3 Processor Tile 0 ...

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Using an FPGA as a Prototyping Platform for Multi-core Processor Applications - group of 3

»

CR Clark, R Nathuji, HHS Lee - 1st Workshop on Architecture Research using FPGA Platforms, ..., 2005 - users.ece.gatech.edu

... an FPGA for the development and **testing** of software for **multi-core** processors, we ...

Inter-**processor** communication is conducted using the shared memory ...

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... of product failure rate to the component soft error rates in a multi-core digital signal

processor

X Zhu, R Baumann, C Pilch, J Zhou, J Jones, C ... - Reliability Physics Symposium, 2005. Proceedings. 43rd ..., 2005 - ieeexplore.ieee.org

... SER) for an advanced 0.13 p, **multi-core**, voice-over-packet digital signal **processor**

(DSP) system ... validation of the standard component SER **testing** methods ...

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Panel discussion for" have we overcome the challenges associated with soc and multi-core testing?

N Chelstrom - Test, 2005 IEEE International Conference on, 2005 - ieeexplore.ieee.org

... or repetitive cores in a **Multi-core** design, there ... **Testing** these efficiently, in multiple clock domains, presents a challenge which the CELL **processor** was able ...

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Multi-Core Systems for Digital Video Reception and Video Processing

G Burns, M Jacobs, M Lindwer, B Vandewiele - 2005 - silicon-hive.com

... **Multi-Core** System for Digital Video Reception ... Bottom-up verification of **processor**

building blocks was not ... Conformance **testing** of the complete system (SoC and ...

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Testability features of the first-generation cell processor

M Riley, L Bushard, N Chelstrom, N Kiryu, S ... - Test, 2005 IEEE International Conference on, 2005 - ieeexplore.ieee.org

... **Multi-core** processing elements ... will enable low cost testers and multi-device **testing**. ... capable of managing different numbers of **processor** cores efficiently. ...

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Debug Support, Calibration and Emulation for Multiple Processor and Powertrain Control

SoCs - group of 4 »

A Mayer, H Siebert, KD McDonald-Maier - Proceedings of the Design, Automation and Test in Europe (..., 2005 - portal.acm.org

... Increased **testing** and more effective debugging could reduce this by a third [3].
System ... When **processor** cores can be stopped the **multi- core** break and ...
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High-End Server System Partitioning for Cost Reduction - group of 3 »

GA Katopis, T Zhou, M Thornton - Advanced Packaging, IEEE Transactions on [see also ..., 2006 -
[ieeexplore.ieee.org](#)

... It is to be noted that in the **multicore processor** chip era ... a single functional core
on a **processor** chip necessitates the same chip processing and **testing** as all ...
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Simulating and debugging **multicore** behavior - group of 2 »

EE TIMES, EET CHINA, EET GERMANY, EET KOREA, EET ... - [embedded.com](#)

... Even the workhorse PC is deploying **multicore** chips from ... in much of the regression
testing, final **testing**, and even ... limit to the number of **processor** cores, with ...
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The Challenges of Parallelism - group of 3 »

C Hill - [cs.uwec.edu](#)

... by the new types of errors that are added to the **testing** problem. ... The determination
is that a **multi-core processor** could deliver more speed with less heat. ...
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JA Fisher - Design Automation, 1999. 36th Annual Conference on, 1999 - [ieeexplore.ieee.org](#)... As put in an **Intel white paper**, published on the web ... Because binary compatibility is so important, **Intel** has provided ... 7. Summary In this **paper** I have outlined ...Cited by 35 - [Web Search](#) - [BL Direct](#)

XTREM: a power simulator for the Intel XScale® core - group of 2 »

G Contreras, M Martonosi, J Peng, R Ju, GY Lueh - Proceedings of the 2004 ACM SIGPLAN/SIGBED conference on ..., 2004 - [portal.acm.org](#)... JVM for all the experiments presented in this **paper**. ... The **white** bar in Figure 6 corresponds to the ... adequately many aspects of the **Intel XScale** microarchitecture ...Cited by 10 - [Web Search](#) - [BL Direct](#)

Realizing the Benefits of Structural Test for Intel Microprocessors - group of 8 »

M Mayberry, J Johnson, N Shahriari, M Tripp - Proceedings of the 2002 IEEE International Test Conference, 2002 - [doi.ieeecomputersociety.org](#)... As an example, the tester used for the **Intel® Pentium® II** ... For transition scan tests, however, **core** logic clocking required precise clocking ... 458 **Paper** 16.3 ...Cited by 10 - [Web Search](#) - [BL Direct](#)

A White Paper on the Benefits of Chipkill-Correct ECC for PC Server Main Memory - group of 5 »

TJ Dell - IBM Microelectronics Division Whitepaper, Nov, 1997 - [www-03.ibm.com](#)... fully recognize, even though at it's **core**, it is a ... the Need for Parity in Desktop PCs," **Intel Apps Fax** ... A **White Paper** on the Benefits of Chipkill-Correct ECC ...Cited by 28 - [View as HTML](#) - [Web Search](#)

Characterizing processor architectures for programmable network interfaces - group of 20 »

P Crowley, ME Fluczynski, JL Baer, BN Bershad - Proceedings of the 14th international conference on ..., 2000 - [portal.acm.org](#)... performance **processor** architectures evaluated in this **paper**. ... **processor** parts, such as **Intel's IXP1200** [10 ... of- order, superscalar microprocessor **core** and memory ...Cited by 79 - [Web Search](#)

Automating the design of SOC's using cores - group of 3 »

... , C Fellenz, M Muhlada, F White, JM Daveau, WR Lee - Design & Test of Computers, IEEE, 2001 - [ieeexplore.ieee.org](#)... Foster **White Intel** ... Designers must define address maps for all cores and pass the values as parameters to each **core**, ensuring that an ... **Processor** local bus (PLB) ...Cited by 38 - [Web Search](#) - [BL Direct](#)

Cluster Computing White Paper - group of 3 »

M Baker... - Arxiv preprint cs.DC/0004014, 2000 - [arxiv.org](#)... time and effort to put together this **White Paper**. ... chapters and making the overall **paper** an excellent ... with popular architectures including the **Intel Pentium II** ...Cited by 46 - [View as HTML](#) - [Web Search](#) - [Library Search](#)

White Paper - group of 72 »

IM Technology - HP Part No - panopticon.com

... 27 5.3 Development and **Testing** Facts ... to visualize complex data, and at the **core** of the ... This **white paper** provides an introduction to Panopticon's heat mapping ...Cited by 12 - [View as HTML](#) - [Web Search](#)**TWO RESEARCH CONTRIBUTIONS IN 64-BIT COMPUTING: TESTING AND APPLICATIONS** - group of 2 »

V Chang - eprints.ecs.soton.ac.uk

... computing, and the current direction from Intel, AMD, Microsoft ... better than a 64-bit single-core processor, but only ... 0-7821-2968-4. [2] AMD **White Paper**, "x-86 ...Cited by 1 - [View as HTML](#) - [Web Search](#)**Human-computer interaction using eye-gaze input** - group of 2 »TE Hutchinson, KP **White** Jr, WN Martin, KC Reichert ... - Systems, Man and Cybernetics, IEEE Transactions on, 1989 - ieeexplore.ieee.org... In this **paper** we describe a unique prosthetic device called ... black-and-white video monitor (located to the left ... on a metal sheath, which slides along the tube. ...Cited by 103 - [Web Search](#)

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